

**SANKALCHAND PATEL COLLEGE OF ENGINEERING, VISNAGAR**  
**COMPUTER ENGINEERING DEPARTMENT**  
**B.E. Semester – VI (Computer Engineering)**

**ASSIGNMENT - 2**

**Subject: System Programming (160706)**

**Date: 24/03/2014**

<b>Q.1</b>	Explain analysis and synthesis phases of an assembler by clearly stating their tasks.																				
<b>Q.2</b>	Describe following data structures: OPTAB, SYMTAB, LITTAB and POOLTAB.																				
<b>Q.3</b>	Explain and show usage by giving examples of following assembler directives: ORIGIN, EQU, LTORG, START.																				
<b>Q.4</b>	Explain & compare various intermediate code forms (representations) for an assembler.																				
<b>Q.5</b>	Define two macros of your choice to illustrate nested calls to these macros. Also show their corresponding expansion.																				
<b>Q.6</b>	Explain with examples - expansion time variables, expansion time statements - AIF and AGO for macro programming. Show their usage for expansion time loop by giving example.																				
<b>Q.7</b>	Explain attributes of formal parameters, default specifications of parameter and semantic expansion for macro by giving examples.																				
<b>Q.8</b>	<p>Let us consider a two pass assembler and assume that each instruction is one word. Given an assembly program and code for Mnemonics.</p> <table border="1" style="width: 100%; border-collapse: collapse;"> <tr> <td style="width: 50%; padding: 5px;"> <pre> START 101 READ  A READ  B MOVER BREG, A MULT  BREG, B MOVEM BREG, D STOP A    DS 1 B    DS 1 D    DS 1 END </pre> </td> <td style="width: 50%; padding: 5px;"> <table style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th style="text-align: left; border-bottom: 1px solid black;">Mnemonics</th> <th style="text-align: left; border-bottom: 1px solid black;">CODE</th> </tr> </thead> <tbody> <tr><td>STOP</td><td>00</td></tr> <tr><td>MULT</td><td>03</td></tr> <tr><td>MOVER</td><td>04</td></tr> <tr><td>MOVEM</td><td>05</td></tr> <tr><td>READ</td><td>09</td></tr> <tr><td>DS</td><td>02</td></tr> <tr><td>START</td><td>01</td></tr> <tr><td>END</td><td>02</td></tr> </tbody> </table> <p>Ordinal number of BREG is 2</p> </td> </tr> </table> <p>(i) Show content of symbol table at the end of pass-one of an assembler.  (ii) Write intermediate code representation of the assembly program. Use variant-II of intermediate code representation.</p>	<pre> START 101 READ  A READ  B MOVER BREG, A MULT  BREG, B MOVEM BREG, D STOP A    DS 1 B    DS 1 D    DS 1 END </pre>	<table style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th style="text-align: left; border-bottom: 1px solid black;">Mnemonics</th> <th style="text-align: left; border-bottom: 1px solid black;">CODE</th> </tr> </thead> <tbody> <tr><td>STOP</td><td>00</td></tr> <tr><td>MULT</td><td>03</td></tr> <tr><td>MOVER</td><td>04</td></tr> <tr><td>MOVEM</td><td>05</td></tr> <tr><td>READ</td><td>09</td></tr> <tr><td>DS</td><td>02</td></tr> <tr><td>START</td><td>01</td></tr> <tr><td>END</td><td>02</td></tr> </tbody> </table> <p>Ordinal number of BREG is 2</p>	Mnemonics	CODE	STOP	00	MULT	03	MOVER	04	MOVEM	05	READ	09	DS	02	START	01	END	02
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<b>Q.9</b>	Compare single pass assembler and two pass assembler. Explain two pass assembler in detail with suitable example.																				
<b>Q.10</b>	Write a macro which takes B, C, and D as parameters and calculates $B*C + C*D$ .																				
<b>Q.11</b>	Explain advanced macro facilities with suitable example.																				
<b>Q.12</b>	Explain following terms with suitable example. (i) Expansion time variable (iii) Semantic Expansion (ii) Positional parameter (iv) Macro Preprocessor																				
<b>Q.13</b>	What is macro in programming language. Write an algorithm for macro definition.																				
<b>Q.14</b>	List and explain tasks involved in macro expansion.																				